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## \*BIBDATASHEET\*

Bib Data Sheet

CONFIRMATION NO. 1876

SERIAL NUMBER 10/631,303	FILING DATE 07/29/2003  RULE	CLASS 118	GROUP ART UNIT 1763	ATTORNEY DOCKET NO. A6311/T040320US
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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CIP of 09/502,126 02/10/2000

✓ 11/1/04

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

NONE 11/1/04

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/24/2003

Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	CA	22	60	4
Examiner's Signature	Initials			

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## TITLE

Process and an integrated tool for low k dielectric deposition including a pecvd capping module